

DERWENT-ACC-NO: 1991-005905  
DERWENT-WEEK: 199101  
COPYRIGHT 1999 DERWENT INFORMATION LTD

TITLE: Computer faults-errors simulator e.g. for reliability studies - has  
output of third bus shapers unit connected to digital inputs of memory units

INVENTOR: PANKOV, A P; POLOVINKOV, V S ; TANASEICHU, V M

PATENT-ASSIGNEE: OMSK AVTOMATIKA[OMAVR], OMSK POLYMATIKA[OMPO]

PRIORITY-DATA: 1987SU-4345362 (November 9, 1987)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
SU 1564628 A	May 15, 1990	N/A	000	N/A

APPLICATION-DATA:

PUB-NO	APPL-DESCRIPTOR	APPL-NO	APPL-DATE
SU 1564628A	N/A	1987SU-4345362	November 9, 1987

INT-CL\_(IPC): G06F011/26

ABSTRACTED-PUB-NO: SU 1564628A

BASIC-ABSTRACT: The computer faults and error simulator includes memory units (17,18), encoder (18), registers (19-22), bus shapers (23-29), address decoder (31), clock pulse generator (32), multiplexer (33), RS flipflops (34-36) and pulse shaper (37). The operation principle is based on distortion of data (address-data) during its transmission over the interfaces in a tested system (computer). The memory (17) is for storage distortion code for each simulated fault.

USE/ADVANTAGE - For experimental studying reliability and stability of  
redundant structure computing systems contg. different async. interfaces.  
Wider functional scope. Bul.18/15.5.90.

CHOSEN-DRAWING: Dwg.1/3

TITLE-TERMS:

COMPUTER FAULT ERROR SIMULATE RELIABILITY STUDY OUTPUT THIRD BUS  
SHAPE UNIT  
CONNECT DIGITAL INPUT MEMORY UNIT

DERWENT-CLASS: T01

EPI-CODES: T01-G02; T01-G09;

SECONDARY-ACC-NO:

Non-CPI Secondary Accession Numbers: N1991-004583